**NBKR INSTITUTE OF SCIENCE & TECHNOLOGY:: VIDYANAGAR**

**M.Tech (ECE) – I YEAR -II SEMESTER, Digital Electronics & Communication Systems**

SCHEME OF INSTRUCTION AND EVALUATION (With effect from the batch admitted in the academic year 2017-2018)

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| S.No | Course  Code |  | Instruction  Hours/  Week | | | Credits | Evaluation | | | | | | | |
| Sessional  Test-I | | Sessional  Test-II | | Total Sessional Marks (Max. 40) | End Semester  Examination | | Max  Total Marks |
| **THEORY** | L | T | P/D |  | Duration  In Hours | Max.  Marks | Duration  In Hours | Max.  Marks | 0.8\*Best of 2 mids + 0.2\* other mid | Duration  In Hours | Max.  Marks |  |
| 1 | 17EC1201 | Wireless Communications | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 2 | 17EC1202 | Digital Communication Techniques | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 3 | 17EC1203 | Modern Radar Systems | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 4 | 17EC1204 | Adaptive Signal Processing | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 5 | 17EC1205 | Micro-Computer System Design | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
| 6 |  | Elective -II | 4 | - | - | 4 | 2 | 40 | 2 | 40 | 3 | 60 | 100 |
|  | | **PRACTICALS** |  |  |  | | | | | |  |  | | |
| 7 | 17EC12P1 | Communications and Signal Processing Lab | - | - | 3 | 2 | Day to Day Assessment | | | 40 | Day to Day Evaluation(30) + a test(10) (40Marks) | 3 | 60 | 100 |
| 8 | 17EC12P2 | Seminar II | - | - | 4 | 2 | Periodical Review and Internal Seminar | | | 100 | Continuous Assessment (100) | - | - | 100 |
|  |  | **TOTAL** | **24** | **-** | **07** | **28** | **-** | **-** | **-** | **-** |  | **-** | **-** | **800** |